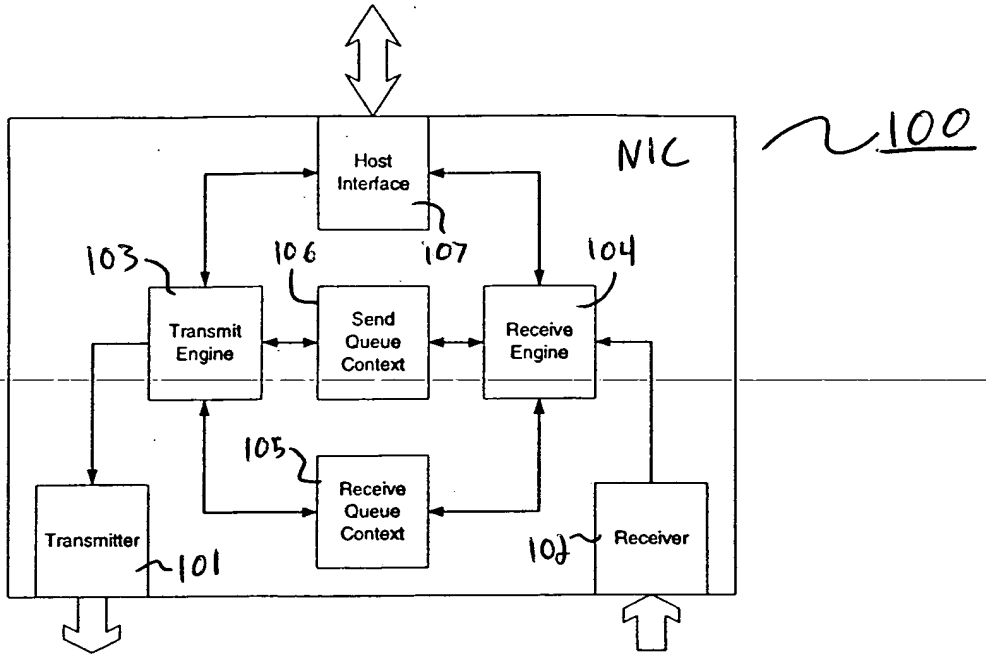
NIC
100
Host Interface
107
103
106
104
Transmit Engine
Send Queue Context
Receive Engine
105
Receive Queue Context
Transmitter
101
102
Receiver

Fig. 1

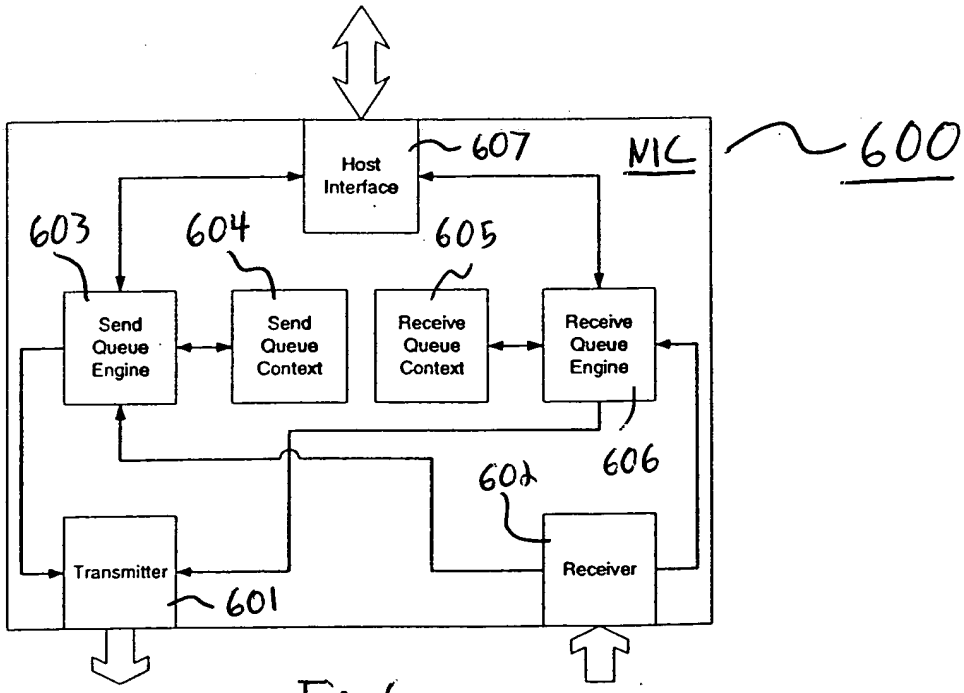
NIC
600
Host Interface
607
603
604
605
Send Queue Engine
Send Queue Context
Receive Queue Context
Receive Queue Engine
606
602
Transmitter
601
Receiver

Fig. 6

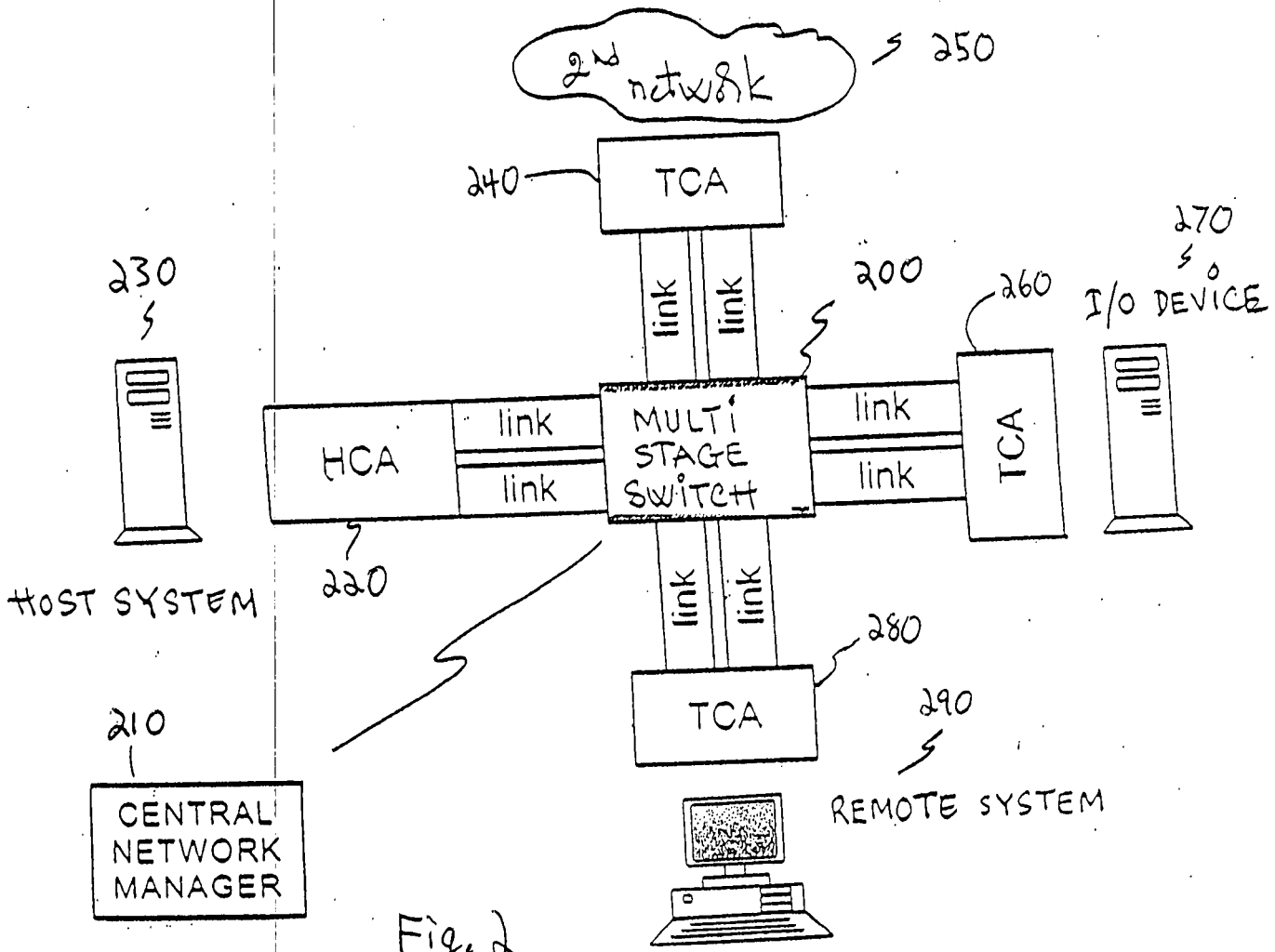

Fig. 2

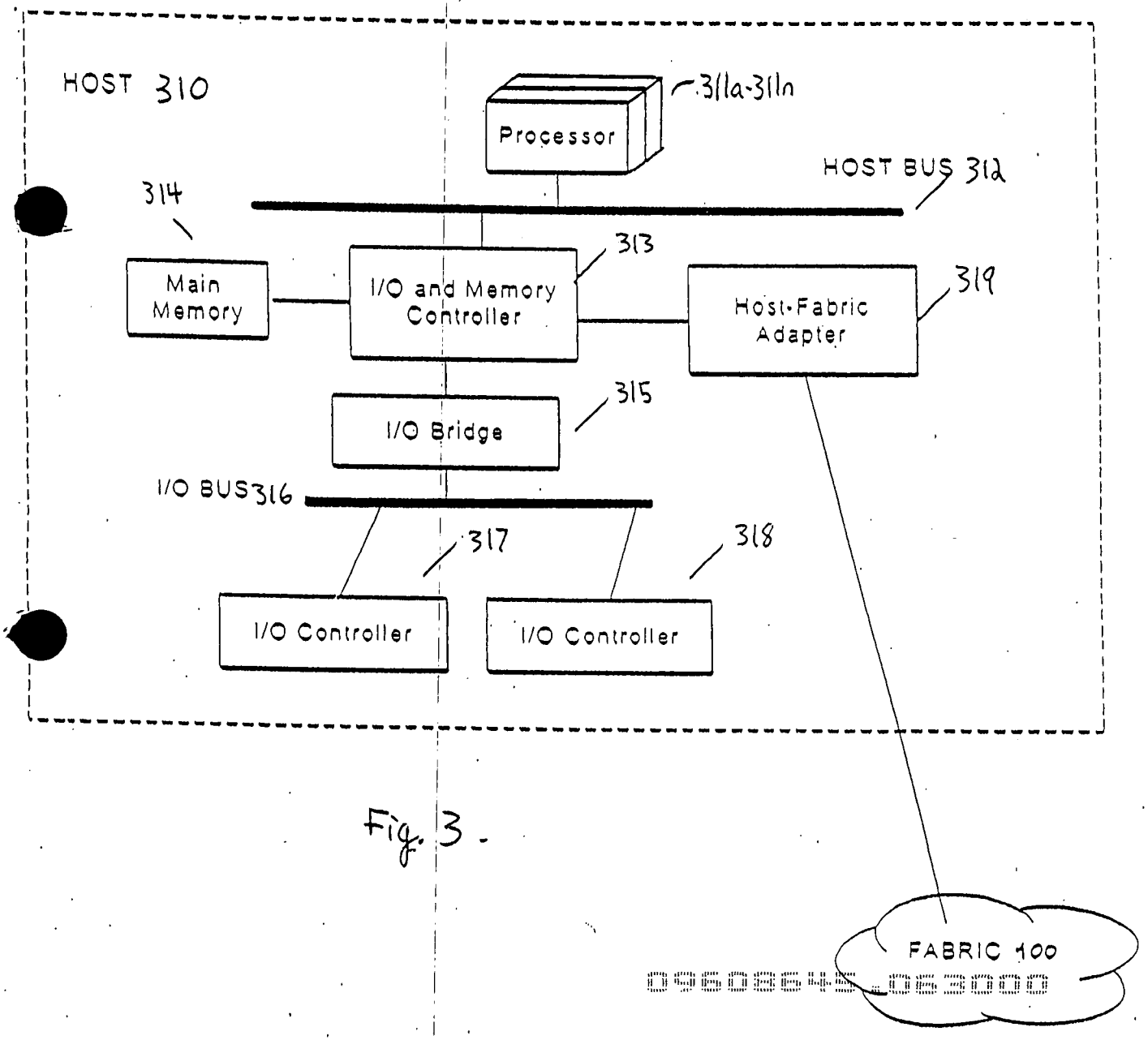
HOST 310
Processor
311a-311n
HOST BUS 312
314
313
Main Memory
I/O and Memory Controller
319
Host-Fabric Adapter
315
I/O Bridge
I/O BUS 316
317
318
I/O Controller
I/O Controller
FABRIC 100

Fig. 3.

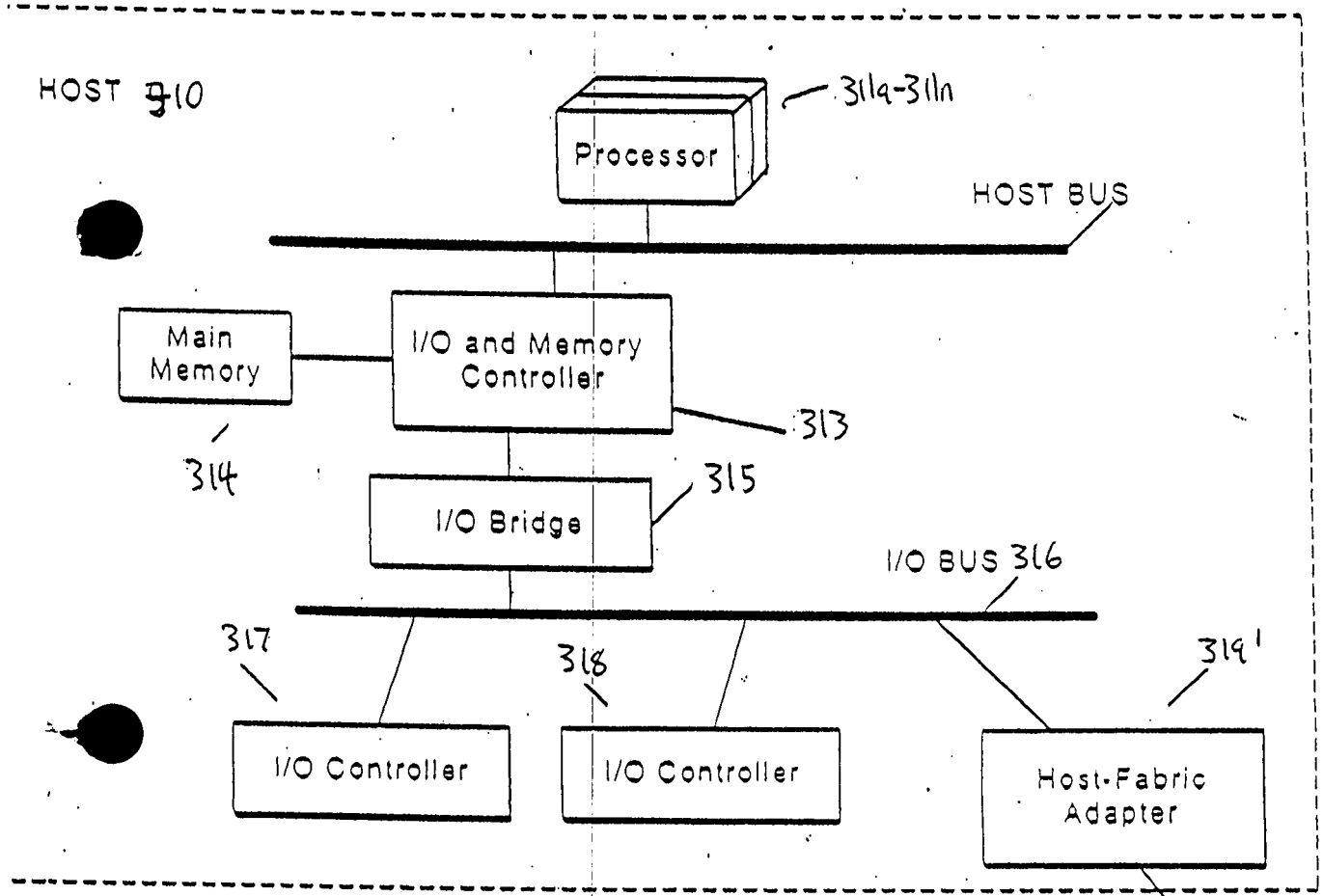

Fig. 4

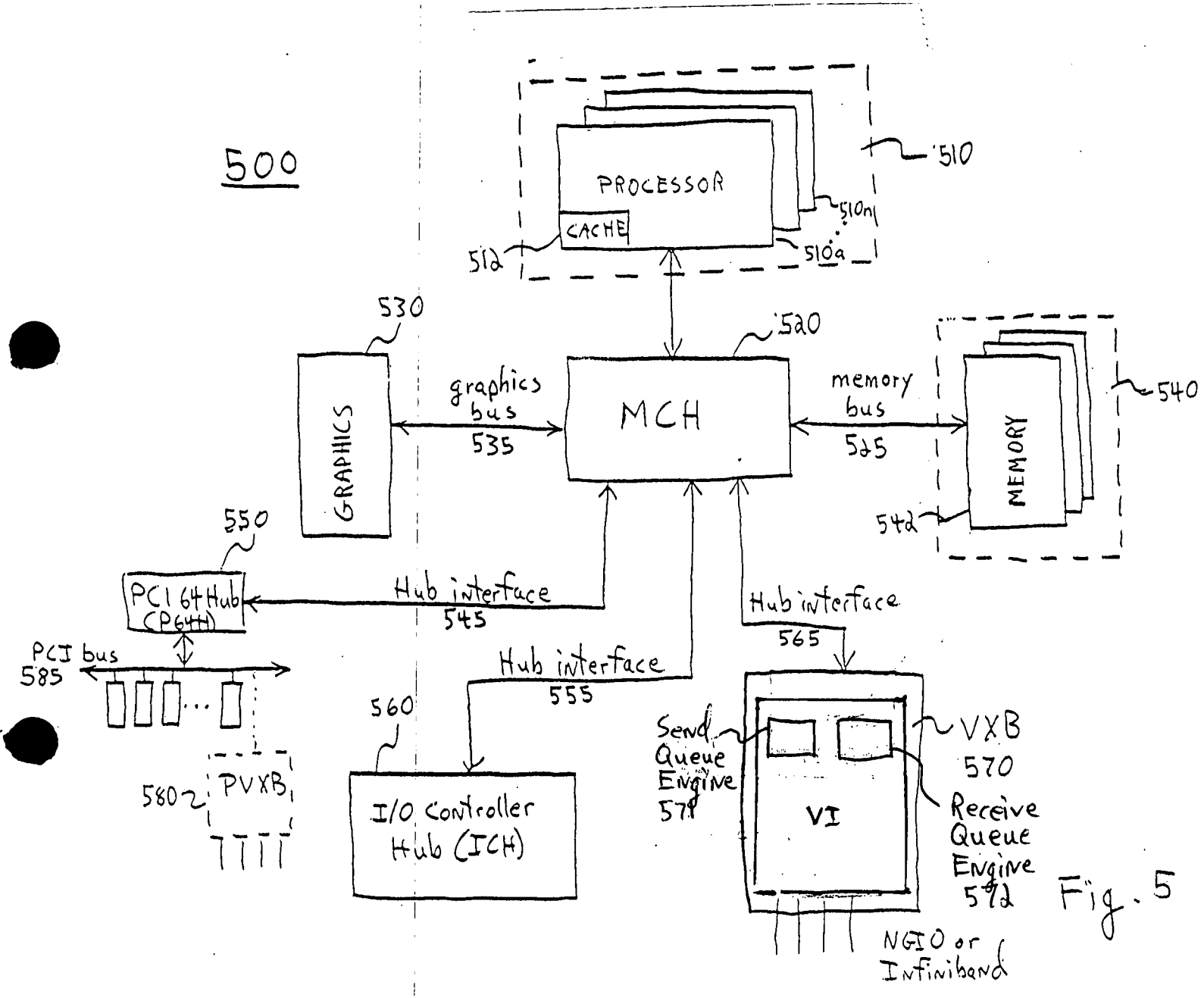

Fig. 5

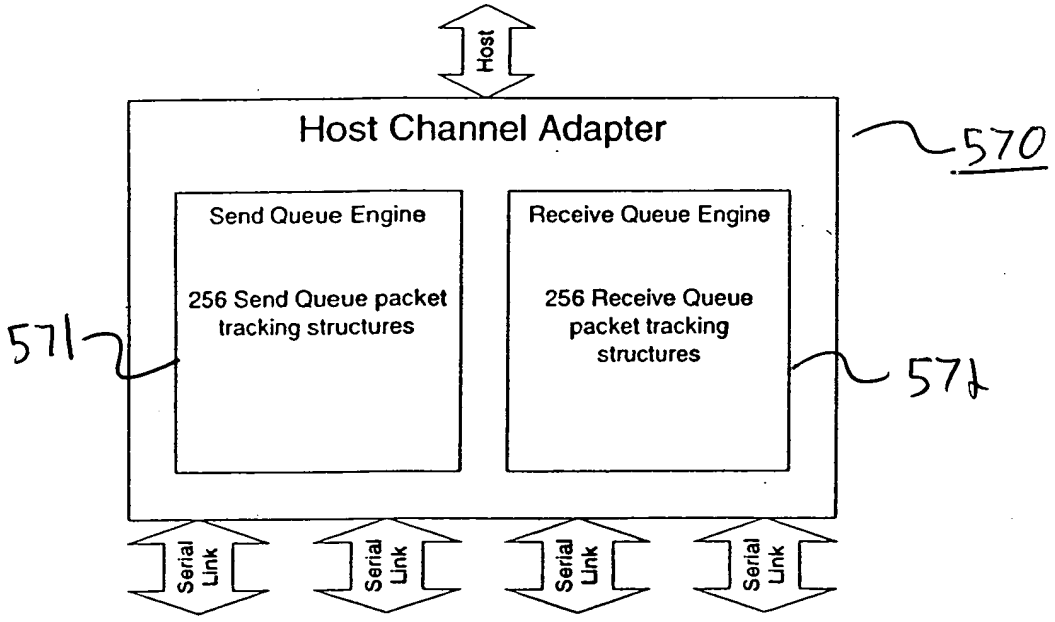
Host
Host Channel Adapter
570
Send Queue Engine
256 Send Queue packet tracking structures
571
Receive Queue Engine
256 Receive Queue packet tracking structures
571
Serial Link
Serial Link
Serial Link
Serial Link

Fig. 7

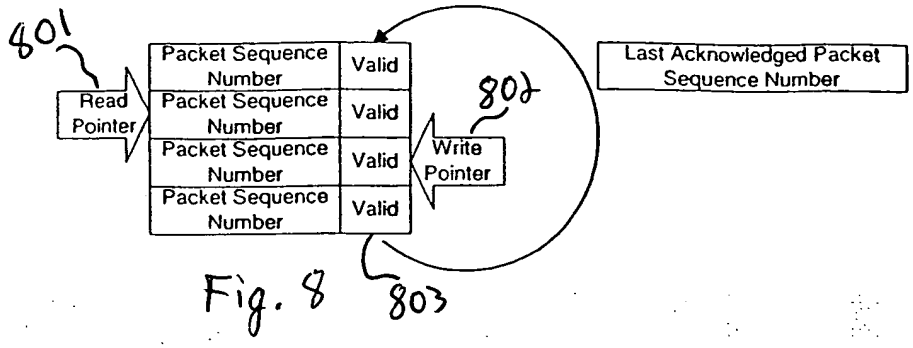

Fig. 8

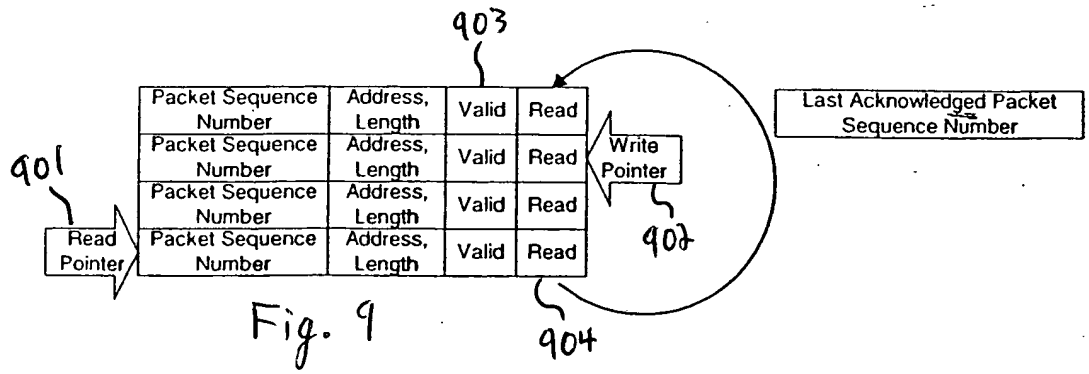

Fig. 9

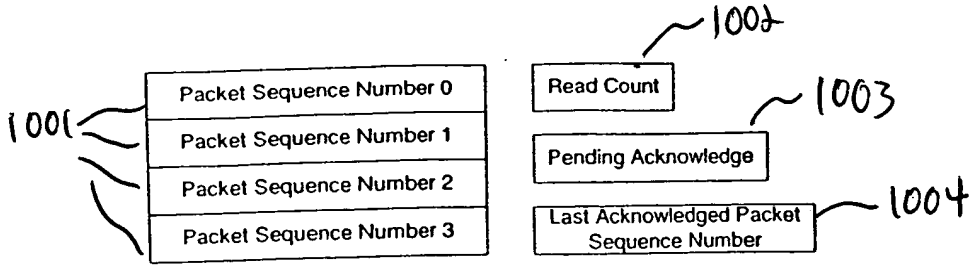

Fig. 10

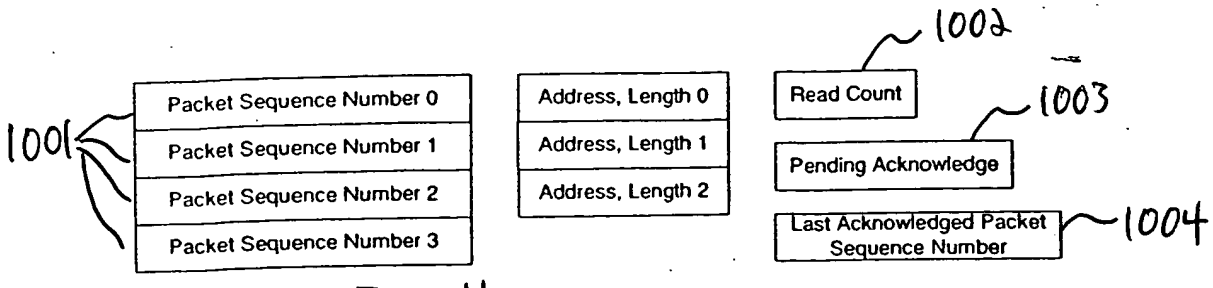

Fig. 11